

GOVERNMENT POLYTECHNIC PANAJI
DEPARTMENT OF ELECTRONICS ENGINEERING
Time Table - ODD TERM 2024-25

HALL NO.: 305

Class:	Third Semester								
Date/Time	9:00 am-10:00am	10:00am-11:00am	11:15am- 12:15pm		12:15pm- 1:15pm		2:00pm-3:00pm	3:00pm-4:00pm	4:00pm-5:00pm
Monday		CNW 305 BSN	BTX Practical - Applied Lab				CNW Practical - Comm. Lab		
			Batch A	MS			Batch A	BSN	
			Batch B	CE - Comm. Lab			Batch B	DE - Applied Lab	
Tuesday	DE	BTX	EL - Machines Lab				CP - Digital Lab		
	305 VL1	305 MS	Batch C	SA/SA			Batch C	VL2	
			EL	SA	305	CE			
Wednesday	DE - Applied Lab		EL		CNW		CE		CP - Digital Lab
	Batch A	VL1	305	SA	305	BSN	305	KS	Batch A
	CP - Digital Lab								Batch B
	Batch B	VL2							MS
Thursday	CNW - Comm. Lab		EL - Machines Lab				BTX		CE - Comm. Lab
	Batch C	BSN/BSN	Batch A	SA/SA			305	MS	Batch A
			CP - Digital Lab						KS
			Batch B	VL2					Batch B
Friday	EL		DE				CP - Digital Lab		
	305	SA	305	VL1					VL2
			EL - Machines Lab				CNW - Comm. Lab		
			Batch C	VL1			BSN/BSN		

Staff Details	
MS	: Manisha Sajane
BSN	: Bhalchandra S Nadkarni
SP	: Sheetal Pednekar
SA	: Seema Avde
VL1	: Visiting Lecturer 1
VL2	: Visiting Lecturer 2
VL3	: Visiting Lecturer 3

Subject Details	
CNW	: Circuits and Networks
BTX	: Basic Electronics
CP	: Programming in C
EL	: Basic Electrical
DE	: Digital Electronics
CE	: Communication Engineering

Please Note: All free Slots shall be treated as Library Slots

Lecture Hours 15 Practical Hours 14 Total Hours 29

Chanda
Coordinator

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Chairman Time Table

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Principal, GPP